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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/010,737	12/07/2001	Atila Alvandpour	884.451US1	2666

7590 10/02/2002

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EXAMINER	
TRAN, ANH Q	

ART UNIT	PAPER NUMBER
2819	

DATE MAILED: 10/02/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application N .	Applicant(s)	
	10/010,737	ALVANDPOUR ET AL.	
	Examiner	Art Unit	
	Anh Q. Tran	2819	

-- The MAILING DATE f this communication appears on the c ver sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *07 December 2001* .

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-25* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-25* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *07 December 2001* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)☐ The proposed drawing correction filed on _____ is: a)☐ approved b)☐ disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12)☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____ .

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) ☐ The translation of the foreign language provisional application has been received.

15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) *3* .
4) ☐ Interview Summary (PTO-413) Paper No(s). _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: .

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claims 1-7, 9-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Aoki (5,610,544) in view of Nunogami (5,136,191).

Regarding claims 1 & 11 Aoki shows a logic unit (Fig. 2) comprising:

A first logic unit (2a) connected to a first supply voltage (Vcc);

A second logic unit (3a) connected to a second supply voltage; and

A voltage-level converter or a logic circuit (6a) including at least one transistor (61, Fig. 3) connected to the second supply voltage. Therefore, Aoki discloses the claimed invention except for teaching that the at least one transistor having a threshold voltage greater than or about equal to the difference between the second supply voltage and the first supply voltage.

However, Nunogami teaches that it is known to modify a pull-up transistor threshold voltage greater than or about equal to the difference between the second supply voltage and the first supply voltage (col. 2, lines 1-4). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the at least one transistor (61, Fig. 3) of Aoki having a threshold voltage

greater than or about equal to the difference between the second supply voltage and the first supply voltage, as taught by Nunogami in order to transfer a signal without troubles.

Regarding claim 2, Aoki disclose the first logic unit comprises a memory unit (ROM, col. 1, line 41).

Regarding claim 3, Aoki disclose the second logic unit comprises an arithmetic unit (inherent limitation since a RAM comprise decoder circuitries)

Regarding claims 4-7, see figure 3.

Claim 9-10, Nunogami shows the voltage-level converter comprises a first inverter (14, Fig. 1) coupled in series to a second inverter (17).

The apparatus described above is applicable to the method claims 23-25.

Claims 8 & 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Aoki (5,610,544) in view of Nunogami (5,136,191) and further view of Tanaka et al 96,249,145).

Aoki in view of Nunogami discloses the claimed invention except for the second logic unit comprises a clock distribution circuit. Tanaka discloses a second logic unit (602, Fig. 14) comprises a clock distribution circuit. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to provide the logic unit (3a) of Aoki having a clock distribution circuit, in order to provide clock distribution signals.

Claims 12-22 are rejected under 35 U.S.C. 103(a) as being unpatentable over Aoki (5,610,544) in view of Nunogami (5,136,191), Tanaka et al 96,249,145), Aizaki (5,115,434) and in further view of LaRue et al (5,027,007).

Aoki in view of Nunogami further view of Tanaka disclose the claimed invention except for AND, NAND, OR, NOR, or XOR. LaRue shows the logic circuit (9, Fig. 3) comprises a NOR circuit and LaRue teaches the logic functions (AND, NAND, OR, NOR, or XOR) gates are fundamental logic operations which form by combination logic gate and not gates (col. 2). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was to made to implement the logic circuit comprises AND, NAND, OR, NOR, or XOR circuit in order to provide logical Boolean function.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anh Q. Tran whose telephone number is 703-306-4507. The examiner can normally be reached on M-TH (7:00-5:30) Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Tokar can be reached on 703-305-3493. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7724 for regular communications and 703-308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Anh Tran
September 25, 2002